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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
10/017,676 12/14/2001		Shahram Abdollahi-Alibeik	204.1001.02	9559	
7590 12/17/2004			EXAMINER		
SHAHRAM A	ABDOLLAHI-ALBELK	BATAILLE, PIERRE MICHE			
P.O. BOX 193	89				
STANFORD,	CA 94309		ART UNIT	PAPER NUMBER	
			2186		

DATE MAILED: 12/17/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

		<u> </u>	Appli	cati n No.	Applicant(s)		$ \leftarrow $
			10/0	17,676	ABDOLLAHI-ALIBEII	K ET AL.	d
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			Pierre	e-Michel Bataille	2186		
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11)[_]	The oath or declar	ation is objected to	by the Examine	r. Note the attache	d Office Action or form PTO	-152.	
Priority ι	ınder 35 U.S.C. §	119					
12)	Acknowledgment is	s made of a claim fo	or foreign priority	v under 35 U.S.C.	§ 119(a)-(d) or (f).		
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DETAILED ACTION

- 1. This Office Action is taken in response to Applicant's communication filed October 29, 2004 responding to prior Rejection dated April 28, 2004. Applicant's amendment and/or arguments have been considered with the results that follow.
- 2. Claims 10-34 are now pending in the application under prosecution as claims 1-9 have been canceled and claims 10-34 newly added.

Response to Arguments

3. Applicant's arguments with respect to claims 10-34 have been considered but are most in view of the new ground(s) of rejection.

The examiner, however, would like to respectfully emphasize that certain claims, unlike the application statement of novelty, recite known features as addressed below. With respect to claim 10, the features required in the apparatus claimed are known and pertinent to multiple CAM devices, a plurality of content addressable memory banks storing lookup routing addresses, bank-level content for comparing memory array for each content addressable memory bank, comparator for comparing the contents of said bank level with the memory array for deciding whether search address is in the corresponding content addressable memory bank, and connecting and activation means for connecting to said bank and activating said bank. The unexpected result of "activating only the content addressable memory bank if the routing address is deterministically known to be that memory bank" is disclosed in at least Pereira et al (US 6,711,041) and Clark, II et al (US 6,069,573). Although one single reference is

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applied below, the applicant in invited to study the other references cited before making a final response to this Office Action.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35
U.S.C. 102 that form the basis for the rejections under this section made in this
Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 5. Claims 10-15 and 28-31 are rejected under 35 U.S.C. 102(e) as being anticipated by US 6,711,041 (Pereira et al).

With respect to claims 10, 13, Pepeira discloses the invention as claimed, (Fig. 59 is an illustration) a plurality of content addressable memory banks and bank arrays storing range of routing addresses with search logic for deciding whether search addresses match corresponding content addressable memory bank and a selector for selecting the said content addressable memory bank matching the search. Col. 7, Lines 52 to Col. 8, Line 9 specifically discloses the compare operation and the selective activation of bank matching the routing address and the disabling of other banks not matching the routing address. Fig. 32 and Fig. 37 illustrate block match circuit and matching logic and Fig. 60 illustrates block selector circuit.

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Pereira [Col. 40 Line 66 to Col. 41 Line 40] specifically discloses address circuit with register banks for maintaining class-based addresses to access the CAM array in response to certain read and write instructions; the address circuit maintains a bank of highest-priority-match (HPM) registers to store the device indexes that result from class-based compare operations, where the blocks of the CAM array are partitioned classes of storage (e.g. classes A, B and C) with device index that results from a compare operation in register of the HPM register bank that corresponds to each of the classes, respectively. By this arrangement, read operations which reference the highest priority match addresses on a class basis may be supported such that: a sequence of compare operations at classes A, B and C with desire to read the contents of the highest priority match address for a given class, the appropriate HPM register within the HPM register bank will then be selected to provide the address for the read operation. (See also Col. 44, Lines 23-54; Col. 48, Line 65 to Col. 49, Line 24)

With respect to claims 11-12, 14-15, Pereira discloses each content addressable memory bank associated with a routing prefix length (a particular class with independent width and depth configuration), said content addressable memory to be a binary type memory bank and a priority encoder inside each content addressable memory bank to resolve multiple prefix length is eliminated (each block priority encoder 5212(0)-5212(3) in each bank to output a respective bank index, therefore, multiple classes are eliminated) [Col. 41, Lines 9-17].

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Claims 28-31, reciting features corresponding to claims 11-15 as rejected above, are therefore rejected in view of the same arguments.

Allowable Subject Matter

6. Claims 16-27 and 32-34 are allowed.

Conclusion

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

US 6,069,573 (Clark, II et al) teaching match address signal prioritization in a content addressable memory encoder with the match signal indicative of the CAM cell array in the bank providing.

8. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will

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the statutory period for reply expire later than SIX MONTHS from the date of this final action.

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Pierre-Michel Bataille whose telephone number is (571) 272-4178. The examiner can normally be reached on Mon-Fri (9:30A to 6:00P).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew M. Kim can be reached on (571) 272-4182. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Pierre-Michel Bataille Primary Examiner Art Unit 2186

December 10, 2004

PIERRE BATAILLE PRIMARY EXAMINER